Docket No.: S&ZIO020104

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : ASTRID ELBE ET AL.

Filed : CONCURRENTLY HEREWITH

Title : METHOD OF AND APPARATUS FOR MODULAR
MULTIPLICATION

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

S i r :

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications
are submitted herewith:

U.S. Patent No. 4,870,681 (Sedlak), dated September 26, 1989 and corresponding
German Patent DE 36 31 992 C2 (Sedlak), dated November 5, 1987;

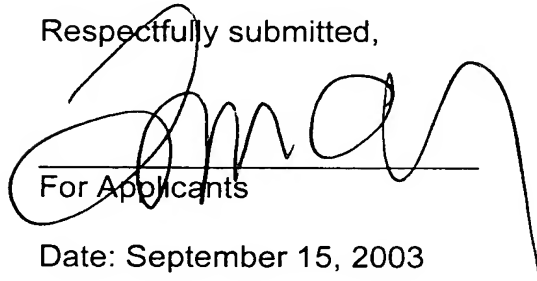PCT WO 99/14880 (Hollmann et al.), dated March 25, 1999;

Walter C. D.: "Faster Modular Multiplication by Operand Scaling", Editor:
Feigenbaum, J.: "Advances in Cryptology-CRYPTO '91", Springer Verlag, August
1991, pp. 1991, pp. 313-323;

Walter, C. D.: "Space/Time Trade-Offs for Higher Radix Modular Multiplication Using
Repeated Addition", IEEE, Vol. 46, No. 2, February 1997, pp. 139-141;

International Search Report, dated September 5, 2002.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,

For Applicants

Date: September 15, 2003

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FORM PTO-1449 (SUBSTITUTE)	Attorney Docket No.: S&ZIO020104
U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	Appl. No.:
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))	Applicant: ASTRID ELBE ET AL.
	Filing Date: September 15, 2003 Group Art Unit:

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A	4,870,681	9/26/89	Sedlak			
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						

FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES	NO
	J	36 31 992 C2	11/5/87	Germany				
	K	99/14880	3/25/99	WIPO				
	L							
	M							
	N							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

		Walter C. D.: "Faster Modular Multiplication by Operand Scaling", Editor: Feigenbaum, J.: "Advances in Cryptology-CRYPTO '91", Springer Verlag, August 1991, pp. 1991, pp. 313-323
		Walter, C. D.: "Space/Time Trade-Offs for Higher Radix Modular Multiplication Using Repeated Addition", IEEE, Vol. 46, No. 2, February 1997, pp. 139-141

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.